



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 3364

SERIAL NUMBER 10/616,623	FILING DATE 07/10/2003 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 03-0932/L13.12-0239
-----------------------------	---------------------------------------	--------------	------------------------	--

APPLICANTS

Aiguo Lu, Pleasanton, CA;

Ivan Pavisic, San Jose, CA;

Nikola Radovanovic, Santa Clara, CA;

** CONTINUING DATA *****

None *P-7*

** FOREIGN APPLICATIONS *****

None *P-7*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 10/14/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 6	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>Michael J. Smith</i> Initials <i>MS</i>				

ADDRESS

Leo J. Peters

LSI LOGIC CORPORATION

1551 McCarthy Boulevard

Milpitas, CA

95035

TITLE

Optimizing IC clock structures by minimizing clock uncertainty

FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
-----------------------------------	---	---